

PPM RECEIVING SYSTEM AND METHOD USING TIME-INTERLEAVED INTEGRATORS

Abstract

A communication receiver (30) includes a data receiver (34) that receives a pulse-position modulated signal (38). A clock circuit (70) separates a reference clock signal (36) into multiple coordinating clock signals A, B, and C. Multiple time integrators (78) are gated to generate multiple time-integrated signals in response to the pulse-position modulated signal (38) and the coordinating clock signals A, B, and C. A combiner (96) forms a demodulated signal from the time-integrated signals IntA, IntB, and IntC.